



IFW

2183

Dkt. 2271/64016

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application of: Shinichi YAMAURA et al.

Serial No.: 09/761,122

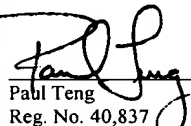
Group Art Unit: 2183

Date Filed: January 16, 2001

Examiner: David J. Huisman

For: **PARALLEL PROCESSOR AND IMAGE PROCESSING APPARATUS ADAPTED  
FOR NONLINEAR PROCESSING THROUGH SELECTION VIA PROCESSOR  
ELEMENT NUMBERS**

I hereby certify that this correspondence is being transmitted by facsimile to the USPTO and is being deposited this date with the U.S. Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

  
Paul Teng  
Reg. No. 40,837

November 1, 2005  
Date

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**Mail Stop Amendment**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**AMENDMENT**

Sir:

This Amendment is submitted in response to the Office Action dated August 3, 2005 in connection with the above-identified application.

Amendments to the Claims are reflected in the **Listing of Claims** section which begins on page 3.

**Remarks** begin on page 6 of this paper.